



US008901993B1

(12) **United States Patent**
Ferran

(10) **Patent No.:** **US 8,901,993 B1**
(45) **Date of Patent:** ***Dec. 2, 2014**

(54) **METHOD AND APPARATUS FOR ACQUIRING NOISE REDUCED HIGH FREQUENCY SIGNALS**

(58) **Field of Classification Search**
CPC A61B 2562/0247; B60R 2021/01516; B60R 2021/01529
USPC 327/91, 94, 379, 551, 552
See application file for complete search history.

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

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This patent is subject to a terminal disclaimer.

(57) **ABSTRACT**

A system and method receive the output signal from a capacitance diaphragm gauge (CDG) and generate a noise reduced output signal. An input signal processing circuit receives an input signal from a signal source that drives the CDG. The input signal processing circuit generates a segment of N normalized digital samples of the input signal. An output signal processing circuit receives the output signal from the CDG and generates M segments of N digital samples of the CDG output signal and averages the corresponding samples in the M segments to generate a signal segment of N averaged samples. Each of the N averaged samples is multiplied by a corresponding one of the N normalized samples to generate N products. The N products are averaged to generate an average product, which is multiplied by a constant to generate a system output signal with reduced noise.

(21) Appl. No.: **14/308,359**

(22) Filed: **Jun. 18, 2014**

Related U.S. Application Data

(63) Continuation of application No. 14/145,831, filed on Dec. 31, 2013, now Pat. No. 8,760,221.

(60) Provisional application No. 61/765,945, filed on Feb. 18, 2013.

(51) **Int. Cl.**
H03B 1/00 (2006.01)
H03K 5/1252 (2006.01)

(52) **U.S. Cl.**
CPC **H03K 5/1252** (2013.01)
USPC **327/551; 327/91**

5 Claims, 4 Drawing Sheets

